

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

James M. Derderian

**Serial No.:** 09/938,106

**Filed:** August 23, 2001

**For:** METHODS FOR ASSEMBLING  
SEMICONDUCTOR DEVICES WITH  
DISCRETE CONDUCTIVE ELEMENTS  
SEPARATING ADJACENT,  
SUPERIMPOSED SEMICONDUCTOR  
DEVICES (as amended)

**Confirmation No.:** 1038

**Examiner:** J. Im

**Group Art Unit:** 2811

**Attorney Docket No.:** 2269-4832US  
(01-0104.00/US)

VIA ELECTRONIC FILING  
July 23, 2007

**AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Examiner:

This Amendment follows the Office Action of April 23, 2007, the shortened statutory period for response to which expires on July 23, 2007.

An amendment to the title appears on page 2 of this paper; and

Remarks start at page 9 of this paper.